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XA-9665A
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

First Named Inventor: AKASAKI, HIROSHI Art Unit: 2824
Appln. No.: 10/823,664 Examiner: V. Nguyen
Filed: April 14, 2004 Conf. No.: 4905
For: SEMICONDUCTOR INTEGRATED CIRCUIT
DEVICE

* * *

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action of October 6, 2005,
please amend the above-identified patent application as
indicated below.

Amendments to the specification begin on page 2 of this
paper.

Amendments to the claims are reflected in the listing
of claims which begins on page 3 of this paper.

Remarks begin on page 6 of this paper.